

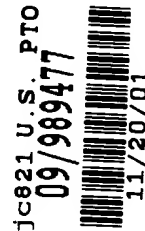
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THORP, REED & ARMSTRONG, LLP  
One Riverfront Center  
Pittsburgh, PA 15222-4895

*Suzanne Horvick*



**PATENT**

**Attorney Docket No.:** DB000624-002

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant:</b>	Mark S. Johnson, et al.	)
		)
<b>Serial No.:</b>	Not Yet Assigned	)
		)
<b>Filed:</b>	31 August 2000	)
<b>Entitled:</b>	OVERMOLDING ENCAPSULATION PROCESS AND ENCAPSULATED ARTICLE MADE THEREFROM	

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Pursuant to the provisions of 37 C.F.R. Sections 1.56, 1.97 and 1.98, Applicants submit the documents listed on the attached PTO Forms PTO/SB/08A PTO/SB/08B for consideration during prosecution of the above-captioned patent application filed herewith.

This statement is made solely for the purpose of compliance with the above-identified rules and is not intended to be a substitute for an independent search by the Examiner and no representation of any such nature is made or intended by this statement.

Also enclosed is a return postcard. Please date stamp and mail the postcard to acknowledge receipt of the above-mentioned correspondence.

Respectfully submitted,



Edward L. Pencoske  
Reg. No. 29,688  
Thorp Reed & Armstrong, LLP  
One Riverfront Center  
Pittsburgh, PA 15222  
(412) 394-7789

Dated: 31 August 2000  
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Attorneys for Applicant

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PTO/SB/08B (10-96)

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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	
		Filing Date	
		First Named Inventor	Mark S. Johnson
		Group Art Unit	2825
		Examiner Name	P. F.
Sheet 1	of 2	Attorney Docket Number	DB000624-002

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
EP		JOHN H. LAU, Chip on Board Technologies for Multichip Modules;	
EP		1994 pp. 2, 3, 24-27, 30-33, 330-337, Van Nostrand Reinhold,	
EP		an International Thomson Publishing Company	
EP		JOHN H. LAU ET AL., "Design, Materials, Processes, Reliability, and Applications," Chip Scale Package (CSP), McGraw-Hill (U.S.A. et al.),	
EP		pp. vii - xvi, 1-2, 43, 61, 71, 87, 97, 107, 129, 143, 157, 173, 183,	
EP		201, 219, 233, 259, 283, 293, 305, 313, 337, 349, 359, 377, 399,	
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<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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